

AN811

The RS-232/DALI Bridge Interface

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INTRODUCTION

The Digitally Addressable Lighting Interface (DALI) has emerged as a standard in Europe to address growing power issues. DALI is known as Annex E of the European electronic ballast standard EN60929 and specifically relates to digitally controlled dimmable ballasts. DALI type ballasts can run at lower power levels than standard magnetic ballasts. With the rise of digitally controlled ballasts, the need for devices that 'talk' to them has increased as well. This application note describes a very simple interface between the RS-232 serial port (available on almost all computers) and DALI, where the PIC16F628 is the core of the interface. The methods and circuits introduced here can be expanded upon to develop more enhanced DALI master control systems.

FIGURE 1: RS-232/DALI BRIDGE



BRIDGING BETWEEN THE TWO INTERFACES

The RS-232/DALI Bridge is a simple translator that transfers data from RS-232 to DALI and vice versa. The Bridge handles the specific interface and conversion requirements for both RS-232 and DALI. Figure 2 illustrates an elementary block view of the Bridge.

FIGURE 2: RS-232/DALI BRIDGE BLOCK DIAGRAM



The RS-232 Forward Data Format

The Bridge is a command driven control, therefore, you must give the bridge a command to transfer the data to the Digitally Addressable Lighting Interface. It receives commands through the RS-232 interface. The data packet length is three bytes. The first byte is the command for the bridge. The second byte is the address information for the DALI device or devices. The third byte is the command information for the DALI device or devices. The format of the data received from the RS-232 serial port is shown in Figure 3.

FIGURE 3: FORWARD DATA FORMAT



The RS-232 Backward Data Format

Data returned from the Digitally Addressable Lighting Interface is transferred through the RS-232 interface in a backward data format, which is different from the forward format. First, an acknowledge byte is transmitted and then a data byte. The acknowledge byte tells the computer if valid data was returned from the Digitally Addressable Lighting Interface. If valid data was returned, then the data is included in the return. Figure 4 illustrates the backward data format.

FIGURE 4: BACKWARD DATA FORMAT



The DALI Forward Frame

The DALI forward frame is 19 bits long using Manchester encoding. The first bit is a start bit. The next sixteen bits are DALI address and command data. The last two are stop bits, which are not Manchester encoded. Figure 5 illustrates the DALI forward frame.

FIGURE 5: DALI Forward Frame



The DALI Backward Frame

The DALI backward frame looks very much like the forward frame with one exception. The backward frame passes only 8 bits of data. Figure 6 illustrates the DALI backward frame.

FIGURE 6: DALI Backward Frame



DALI Command Types

The three basic types of commands for DALI are standard commands, query commands and setup commands. The RS-232/DALI Bridge is programmed with commands to handle each of these three types of commands.

STANDARD COMMANDS

Standard commands are the most common commands. They are used to control arc power. Examples include 'GOTO SCENE LEVEL', 'RECALL MAX LEVEL', and 'STEP UP'. Standard commands are executed with one DALI forward frame. Command 16 of the RS-232/DALI Bridge is used to send standard commands to the Digitally Addressable Lighting Interface.

QUERY COMMANDS

Query commands provide information about the DALI ballast status or setup. Examples include 'QUERY MAX LEVEL', 'QUERY ACTUAL LEVEL', and 'QUERY STATUS'. These commands are executed with one forward frame. Upon execution, the ballast(s) returns data in a DALI backward frame within 9.17 ms. Command 18 on the Bridge is used to send query commands to the Digitally Addressable Lighting Interface.

SETUP COMMANDS

Setup commands are used to setup the ballast(s) on the Digitally Addressable Lighting Interface. Examples of setup commands include 'STORE THE DTR AS MAX LEVEL', 'STORE THE DTR AS SCENE', and 'RESET'. These commands are executed by sending two identical DALI forward frames within 100 ms. Command 17 is used to send setup commands to the Digitally Addressable Lighting Interface.

ADDITIONAL BRIDGE COMMANDS

Beyond sending and receiving DALI commands, the RS-232/DALI Bridge has some other built in commands. These commands primarily focus on the Bridge functionality.

Bridge Status Information

Two pieces of status information about the bridge are available. The most important one is the overload status bit. This identifies if the Digitally Addressable Lighting Interface was shorted for more than 254 ms consecutively. The firmware automatically disables the DALI interface when this event occurs. The second status bit identifies if the bridge actually received data (Bridge command 18). If data was expected, but not received, then this bit is asserted.

Bridge Configuration

There are several commands used to fine-tune the bridge. These commands relate to transmit and receive timings, regulator delays, and command delays. Appendix A identifies all the existing commands corresponding to the firmware. Please refer to source code on our web site at (www.microchip.com).

THE RS-232 INTERFACE

The RS-232 serial interface serves two major purposes. One purpose is to transfer the data from the computer to the Bridge and vice versa. The second purpose is to protect the computer from unexpected high transient voltages. The computer is far more costly to replace than the Bridge.

Isolation

Complimentary circuitry is provided to interface the RS-232 serial connections to the PIC16F628. Essentially, this circuitry is optical isolation, which protects the computer from serious damage, since the Bridge is connected to one or more ballasts. Ballasts typically have running voltages 20 to 40 times greater than the computer serial port logic. Voltages at this level could easily damage the computer. On the RS-232 side, the isolation circuit is powered by the signals of the standard RS-232 serial port. The particular signals providing power are TX, RTS, and DTR. The isolation on the PIC16F628 side is powered by the connected power source. The complete circuit showing the isolation and power requirements is provided in Appendix B.

Transmitting and Receiving

Transmitting and receiving data is very convenient. The design incorporates the use of the USART module built into the PIC16F628. Therefore, all RS-232 serial communications is handled by the hardware on the chip. This allows for simpler firmware.

THE DIGITALLY ADDRESSABLE LIGHTING INTERFACE

DALI is a master-slave type system, which means the slave (ballast) only responds when commanded to. Each ballast responds by sinking the current carried through the two-wire interface, which drops the potential across the two-wire interface. The master (Bridge) provides current regulation to 250mA. Therefore, the master transmits data by raising or lowering the voltage across the two-wire interface. The responding slave transmits data by sinking 250mA of current provided by the master. All data is Manchester encoded. Refer to Appendix B for the circuit design.

Current Regulation

Current regulation of 250mA is provided using one of the comparators built into the PIC16F628. The comparator compares a reference voltage to the voltage across a current sense resistor. This provides the negative feedback. The digital output of the comparator is filtered to supply an analog voltage to the load-driving transistor. This puts the load-driving transistor into its active region. Since the comparator is controlled within the PIC16F628, the regulation can be enabled and disabled as necessary. This feature is particularly useful to prevent circuit failure if the Digitally Addressable Lighting Interface becomes shorted indefinitely. It is also useful to disable regulation when transmitting. Regulation is required for the master to receive data.

Transmitting

The load-driving transistor used for regulation is also used for data transmission; however, the regulation is disabled. The load-driving transistor is switched on or off by the PIC16F628, depending on the data being sent. An additional transistor is switched on or off 180 degrees out of phase. This transistor pulls the data line low when the load-driving transistor is off. Thus data is transmitted using a push-pull transistor configuration. The firmware handles the data transmission.

Receiving

The second comparator in the PIC16F628 is used for receiving data. The reference for this comparator is set slightly below the ripple of the current regulator. When the slave (ballast) is transmitting, the regulator is switching between 250mA of regulation and no regulation. Thus, the data receiving comparator output will always be asserted when regulation is occurring. The

PIC16F628 uses this bit information as the incoming data. The firmware receives these bits of data and compiles them into a useful format.

MEMORY USAGE

The firmware shown on our web site at (www.microchip.com) was built for the PIC16F628. Table 1 provides the percentage used of all the memory types.

Memory Type	Max	Used	% Used
Program	2181	929	42.60
Data	224	42	18.75
EE Data	128	17	13.28

CONCLUSION

The RS-232/DALI Bridge is an example of a simple Digitally Addressable Lighting Interface master device. It performs the required functions that a master device is expected to do on DALI. The basic circuitry and firmware implemented here is a very good starting point for a much more sophisticated or 'smarter' master control.

APPENDIX A: RS-232/DALI BRIDGE COMMANDS

TABLE A-1: RS-232/DALI BRIDGE COMMANDS

Command	Function	DATA1	DATA2
0	Reset the Bridge	NA	NA
1	Return the status of the Bridge, Bit 1 = receive error, Bit 2 = DALI Bus Overload	NA	NA
8	Store first DALI 16-bit sequence, (Command 19)	DALI MSB	DALI LSB
9	Store second DALI 16-bit sequence, (Command 19)	DALI MSB	DALI LSB
10	Store third DALI 16-bit sequence, (Command 19)	DALI MSB	DALI LSB
16	Send, send 16-bit DALI sequence	DALI MSB	DALI LSB
17	Double Send, send 16-bit DALI sequence twice at 10 ms apart	DALI MSB	DALI LSB
18	Send & Receive, send 16-bit DALI sequence and wait at most 100 ms for an 8- bit return	DALI MSB	DALI LSB
19	Send the three word sequence from commands 8, 9, & 10	NA	NA
192	Change DALI TX edge delay (us)	DATA_H	DATA_L
193	Change DALI TX stop delay (us)	DATA_H	DATA_L
194	Change DALI bridge regulator delay (us)	DATA_H	DATA_L
195	Change DALI RX wait (ms) (Command 18)	NA	DATA_L
196	Change DALI RX start delay (us)	DATA_H	DATA_L
197	Change DALI RX sample delay (us)	DATA_H	DATA_L
198	Change DALI RX sample end delay (us)	DATA_H	DATA_L
199	Enable debug	NA	DATA_L
200	Change DALI double send delay (ms) (Command 17)	NA	DATA_L
201	Change DALI sequence delay (ms) (Command 19)	NA	DATA_L
208	Query DALI TX edge delay MSB	NA	NA
209	Query DALI TX edge delay LSB	NA	NA
210	Query DALI TX stop delay MSB	NA	NA
211	Query DALI TX stop delay LSB	NA	NA
212	Query DALI bridge regulator delay MSB	NA	NA
213	Query DALI bridge regulator delay LSB	NA	NA
214	Query DALI RX wait	NA	NA
215	Query DALI RX start delay MSB	NA	NA
216	Query DALI RX start delay LSB	NA	NA
217	Query DALI RX sample delay MSB	NA	NA
218	Query DALI RX sample delay LSB	NA	NA
219	Query DALI RX sample end delay MSB	NA	NA
220	Query DALI RX sample end delay LSB	NA	NA
221	Query debug	NA	NA
222	Query DALI double send delay	NA	NA
223	Query DALI sequence delay	NA	NA

APPENDIX B: THE CIRCUIT



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NOTES:

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03/01/02