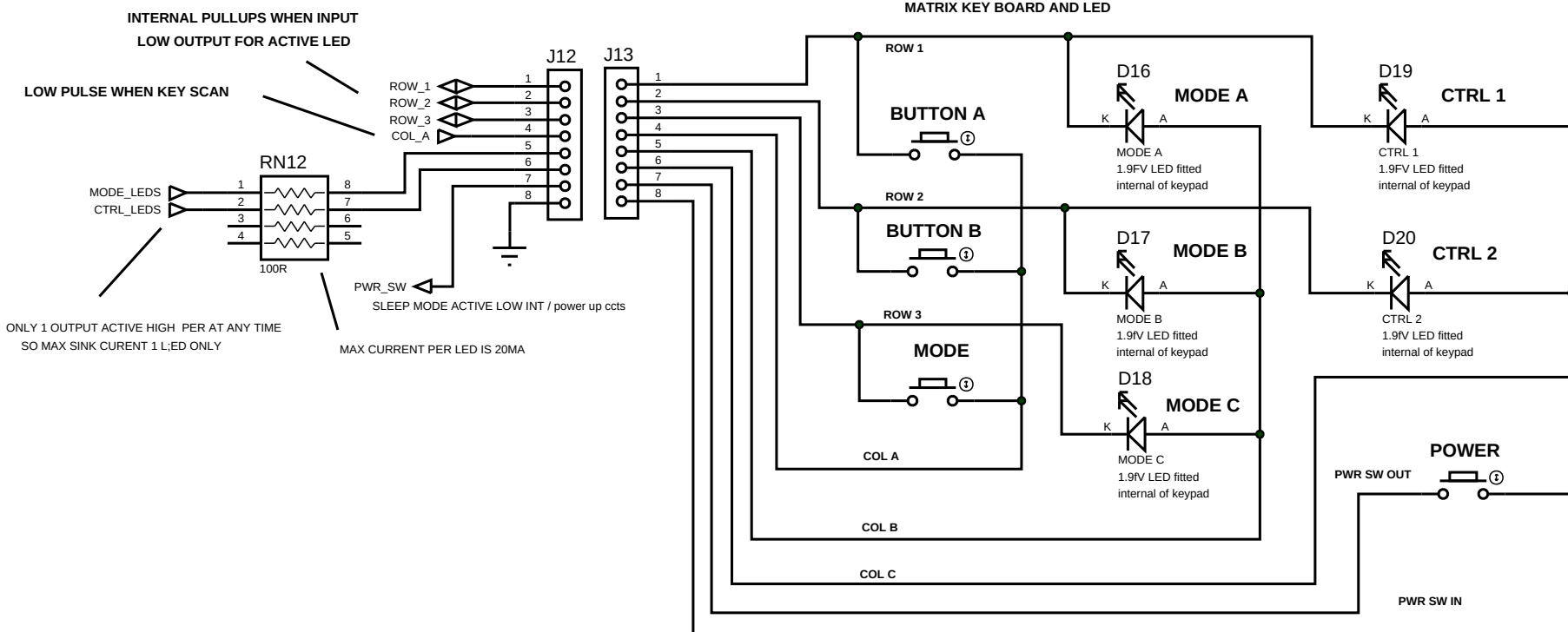


SCAN RATIO 3:1 EG 3 SCANS FOR LED OUTPUT to 1 SCAN KEY INPUT



FILE NAME:	New Project.pdsprj	DATE:	13/03/2015
DESIGN TITLE:	New Project.pdsprj	PAGE:	1
PATH:	C:\Users\SMW OFFICE\Documents\My PIC Projects\Key Gate Timer System	TIME:	3:19:16 PM
BY: @AUTHOR	REV: @REV		