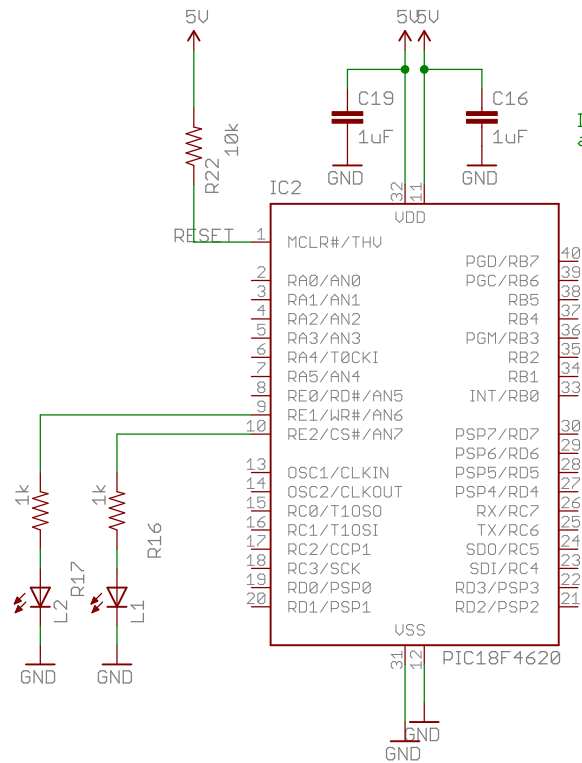




INTRC ClkOut (INTIO1) should use 8 MHz internal oscillator and deliver $f/4 = 2$ MHz on RA6, the ClkOut pin.

Pin numbers for 40 pin package