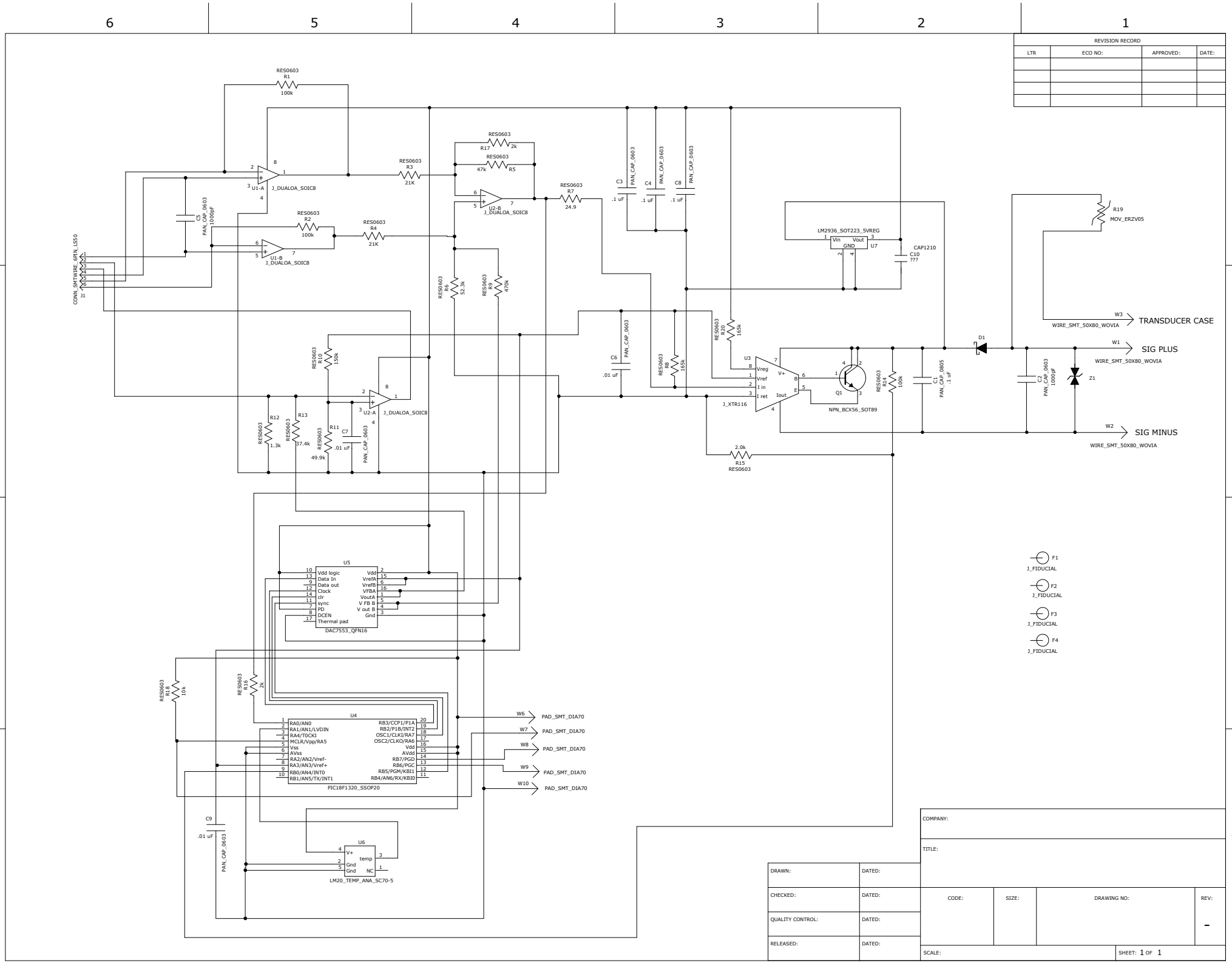
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
RES0603 R1 100k
RES0603 R2 100k
RES0603 R3 21K
RES0603 R4 21K
RES0603 R5 47K
RES0603 R17 2k
RES0603 R7 24.9
U1-A J_DUALOA_SOIC8
U1-B J_DUALOA_SOIC8
U2-A J_DUALOA_SOIC8
U2-B J_DUALOA_SOIC8
C5 PAN_CAP_0603 100pF
C3 PAN_CAP_0603 .1 uF
C4 PAN_CAP_0603 .1 uF
C8 PAN_CAP_0603 .1 uF
C6 PAN_CAP_0603 .01 uF
C7 PAN_CAP_0603 .01 uF
C9 PAN_CAP_0603 .01 uF
CONN_SMTWIRE_6PIN_LS50
J1
RES0603 R6 52.3k
RES0603 R9 470k
RES0603 R8 165k
RES0603 R20 165k
RES0603 R10 150k
RES0603 R12 1.3k
RES0603 R13 37.4k
RES0603 R11 49.9k
RES0603 R14 100k
RES0603 R15 2.0k
RES0603 R16 2k
RES0603 R18 10k
LM2936_SOT223_5VREG
Vin
Vout
GND
U7
CAP1210
C10 ???
R19 MOV_ERZV05
D1
Z1
C1 PAN_CAP_0805 .1 uF
C2 PAN_CAP_0603 1000pF
U3 J_XTR116
Vreg
Vref
I in
I ret
V+
Iout
Q1 NPN_BCX56_SOT89
W3 TRANSDUCER CASE
WIRE_SMT_50X80_WOVIA
W1 SIG PLUS
WIRE_SMT_50X80_WOVIA
W2 SIG MINUS
WIRE_SMT_50X80_WOVIA
F1 J_FIDUCIAL
F2 J_FIDUCIAL
F3 J_FIDUCIAL
F4 J_FIDUCIAL
U5 DAC7553_QFN16
Vdd logic
Data In
Data out
Clock
clr
sync
PD
DCEN
Thermal pad
Vdd
VrefA
VrefB
VFBA
VoutA
V FB B
V out B
Gnd
U4 PIC18F1320_SSOP20
RA0/AN0
RA1/AN1/LVDIN
RA4/TOCKI
MCLR/Vpp/RA5
Vss
AVss
RA2/AN2/Vref-
RA3/AN3/Vref+
RB0/AN4/INT0
RB1/AN5/TX/INT1
RB3/CCP1/P1A
RB2/P1B/INT2
OSC1/CLKI/RA7
OSC2/CLKO/RA6
Vdd
AVdd
RB7/PGD
RB6/PGC
RB5/PGM/KBI1
RB4/AN6/RX/KBI0
W6 PAD_SMT_DIA70
W7 PAD_SMT_DIA70
W8 PAD_SMT_DIA70
W9 PAD_SMT_DIA70
W10 PAD_SMT_DIA70
U6 LM20_TEMP_ANA_SC70-5
V+
temp
Gnd
Gnd
NC
COMPANY:
TITLE:
DRAWN:
DATED:
CHECKED:
DATED:
QUALITY CONTROL:
DATED:
RELEASED:
DATED:
CODE:
SIZE:
DRAWING NO:
REV:
-
SCALE:
SHEET: 1 OF 1